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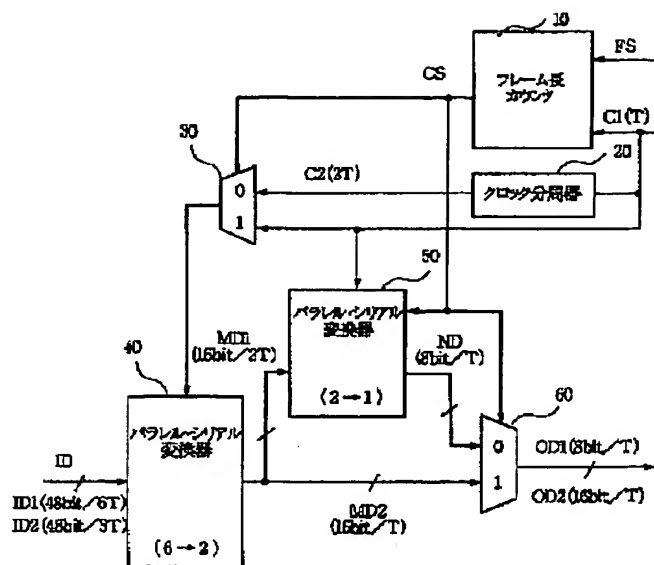
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APPLICANT : NEC COMMUN SYST LTD;

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TITLE : PARALLEL/SERIAL CONVERSION
CIRCUIT



ABSTRACT : PURPOSE: To miniaturize a parallel/serial conversion circuit obtaining output data of variable byte width from input data of prescribed byte width.

CONSTITUTION: A frame length counter 10 measures the pulse interval of a frame signal FS, identifies data width to be outputted and outputs a control signal CS corresponding to data width. A parallel/serial converter 40 in a prestage converts the input data signal ID of six byte width into the data signals MD1 and MD2 of first output byte width (two bytes). A parallel/serial converter 50 in a poststage converts the data signal MD1 into the data signal ND of second byte width (one byte). A data selector 60 selects either the data signal MD2 or ND in accordance with the control signal, and outputs it as an output data signal OD2 or OD1.

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